

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1774	(324/537,522,511,609).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/30 15:08
L2	68	1 and ((detect\$4 measur\$4 inspect\$4 determin\$4)near3 (disconnect\$5))	US-PGPUB; USPAT	OR	ON	2005/06/30 15:15
L3	3	2 and (set\$4 near3 impedance)	US-PGPUB; USPAT	OR	ON	2005/06/30 15:16
L4	8101	((detect\$4 measur\$4 inspect\$4 determin\$4)near3 (disconnect\$5))	US-PGPUB; USPAT	OR	ON	2005/06/30 15:33
L5	95	4 and (set\$4 near3 impedance)	US-PGPUB; USPAT	OR	ON	2005/06/30 15:36
L6	38130	"95" and transistor	US-PGPUB; USPAT	OR	ON	2005/06/30 15:26
L7	49	5 and transistor	US-PGPUB; USPAT	OR	ON	2005/06/30 15:34
L8	38823	((detect\$4 measur\$4 inspect\$4 determin\$4)near3 (disconnect\$5 break\$3 (short near3 circuit\$3)))	US-PGPUB; USPAT	OR	ON	2005/06/30 15:36
L9	313	8 and (set\$4 near3 impedance)	US-PGPUB; USPAT	OR	ON	2005/06/30 15:34
L10	177	9 and transistor	US-PGPUB; USPAT	OR	ON	2005/06/30 15:37
L11	494	((detect\$4 measur\$4 inspect\$4 determin\$4)near3 (disconnect\$5 break\$3 (short near3 circuit\$3))).ti.	US-PGPUB; USPAT	OR	ON	2005/06/30 15:53
L12	177	10 and (set\$4 near3 impedance)	US-PGPUB; USPAT	OR	ON	2005/06/30 15:38
L13	177	12 and transistor	US-PGPUB; USPAT	OR	ON	2005/06/30 15:37
L14	3	11 and (set\$4 near3 impedance)	US-PGPUB; USPAT	OR	ON	2005/06/30 15:38
L15	14	11 and ((suppl\$4 appl\$4 feed\$4)near3 current near5 transistor)	US-PGPUB; USPAT	OR	ON	2005/06/30 15:45
L16	8	15 and impedance	US-PGPUB; USPAT	OR	ON	2005/06/30 15:45
L17	8	15 and (impedance with (set\$4 high\$3 low\$3))	US-PGPUB; USPAT	OR	ON	2005/06/30 15:50
L18	509	(324/522).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/30 16:28
L19	1064	(324/537).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/30 16:26

L20	172	(324/511).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/30 15:50
L21	24	1 and ((detect\$4 measur\$4 inspect\$4 determin\$4)near3 (disconnect\$5 break\$3 (short near3 circuit\$3))).ti.	US-PGPUB; USPAT	OR	ON	2005/06/30 16:06
L22	1	("5422568").PN.	US-PGPUB; USPAT	OR	OFF	2005/06/30 16:06
L23	0	22 and impedance	US-PGPUB; USPAT	OR	OFF	2005/06/30 16:06
L24	0	22 and impedance	US-PGPUB; USPAT	OR	ON	2005/06/30 16:06
L25	0	22 and (resistance)	US-PGPUB; USPAT	OR	ON	2005/06/30 16:06
L26	48	(324/537).CCLS.	US-PGPUB	OR	OFF	2005/06/30 16:26
L27	57	(324/522).CCLS.	US-PGPUB	OR	OFF	2005/06/30 16:29
S2	1	("20040189323").PN.	US-PGPUB; USPAT	OR	OFF	2005/06/30 15:05
S3	7853	(determin\$4 detect\$4 measur\$4)near3 disconnect\$4	US-PGPUB; USPAT	OR	ON	2005/06/28 15:20
S4	12220	(determin\$4 detect\$4 measur\$4 occur\$5)near3 disconnect\$4	US-PGPUB; USPAT	OR	ON	2005/06/28 15:20
S5	132	((determin\$4 detect\$4 measur\$4 occur\$5)near3 disconnect\$4)with (impedance)	US-PGPUB; USPAT	OR	ON	2005/06/28 15:21
S6	607	(324/500,509).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:20
S7	0	S5 and S6	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:20
S8	42	S5 and transistor	US-PGPUB; USPAT	OR	ON	2005/06/28 15:54
S9	2881	(detect\$4 determin\$4 test\$4)near3 disconnection	US-PGPUB; USPAT	OR	ON	2005/06/28 15:54
S10	94	S3 and (set\$4 near3 impedance )	US-PGPUB; USPAT	OR	ON	2005/06/28 15:56
S11	1	S3 and (set\$4 near3 impedance )with (higher lower)with sens\$4 with control\$4	US-PGPUB; USPAT	OR	ON	2005/06/28 15:57
S12	9	S10 and( (suppl\$4 near3 current)with transistor)	US-PGPUB; USPAT	OR	ON	2005/06/28 16:00
S13	12	((disconnection near detect\$4)near2 circuit).ti.	US-PGPUB; USPAT	OR	ON	2005/06/28 16:01
S14	20	((disconnection near detect\$4)near2 circuit).ab.	US-PGPUB; USPAT	OR	ON	2005/06/28 16:01
S15	26	S13 or S14	US-PGPUB; USPAT	OR	ON	2005/06/28 16:01

S16	1	S15 and (impedance with set\$4)	US-PGPUB; USPAT	OR	ON	2005/06/28 16:02
S17	6	S15 and (impedance and transistor)	US-PGPUB; USPAT	OR	ON	2005/06/28 16:02